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•	INFORMATI	ON DISCLO	SURE	Application Number	10/781,855	
	STATEMEN	T BY APPL	ICANT	Filing Date	February 20, 2004	
	0.1.0.1	d. D	- 7. 2005	First Named Inventor	Werner DOETSCH	
	Date Submitte	a: Decembe	r 7, 2005	Group Art Unit	1761	
	(use as many sheets as necessary)			Examiner Name	Chhaya D. Sayala	
Sheet	1	of	1	Attorney Docket Number	038715.53064US	

				F	OREIGN PATENT DOCUMEN	TS		
Examiner Initials*	Cite No.1	Office	oreign Patent D	ocument Kind Code <sup>5</sup> (if known)	Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Τ <sup>¢</sup>
b	AA	JP	62-103002	А	ISHIHARA HIRYO KOGYO KK	05-13-1987		
6	АВ	JP	S61-33104	А	RYO UNO	02-17-1986		×
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Cs	_AC	WOLF et al., Silicon Processing for the VLSI Era, Volume 1 – Process Technology, Chapter 1, pp. 1 – 35, Lattice Press, California, USA				
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